

FIG. 1

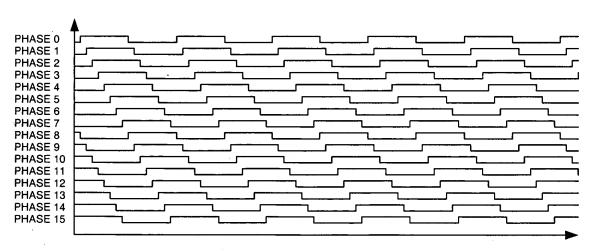
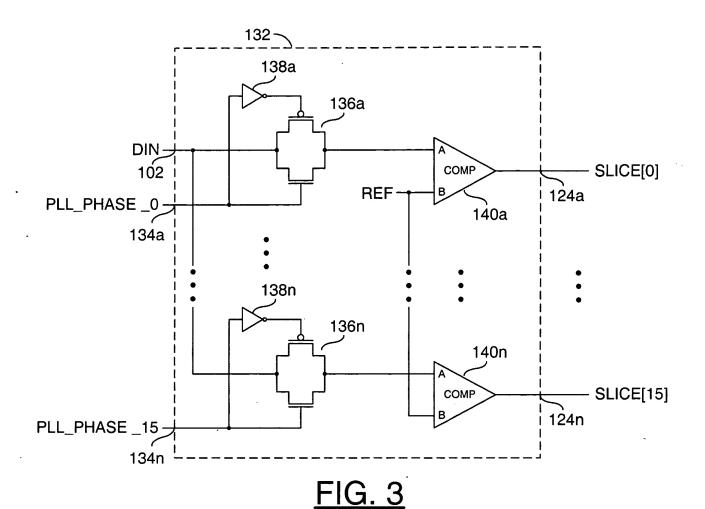


FIG. 2



122~ 144 -142-104 DAT - DOUT SLICE -- OCP 106 125 150 148 108 **SYNC** PLL_PHASE_0 - PACK PLL_PHASES-STROBE **OUTPUT** 127 146 154 152 SYM POL PLL_PHASES 156 116

FIG. 4

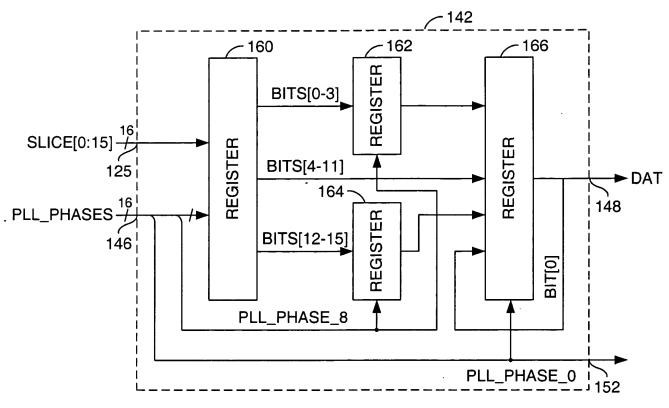
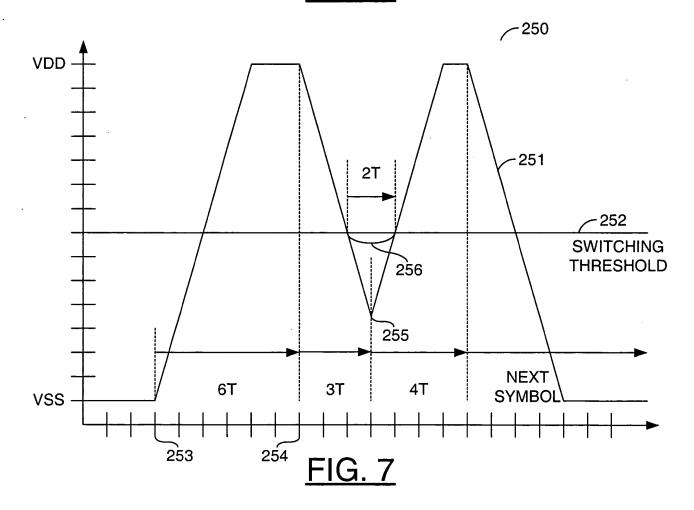
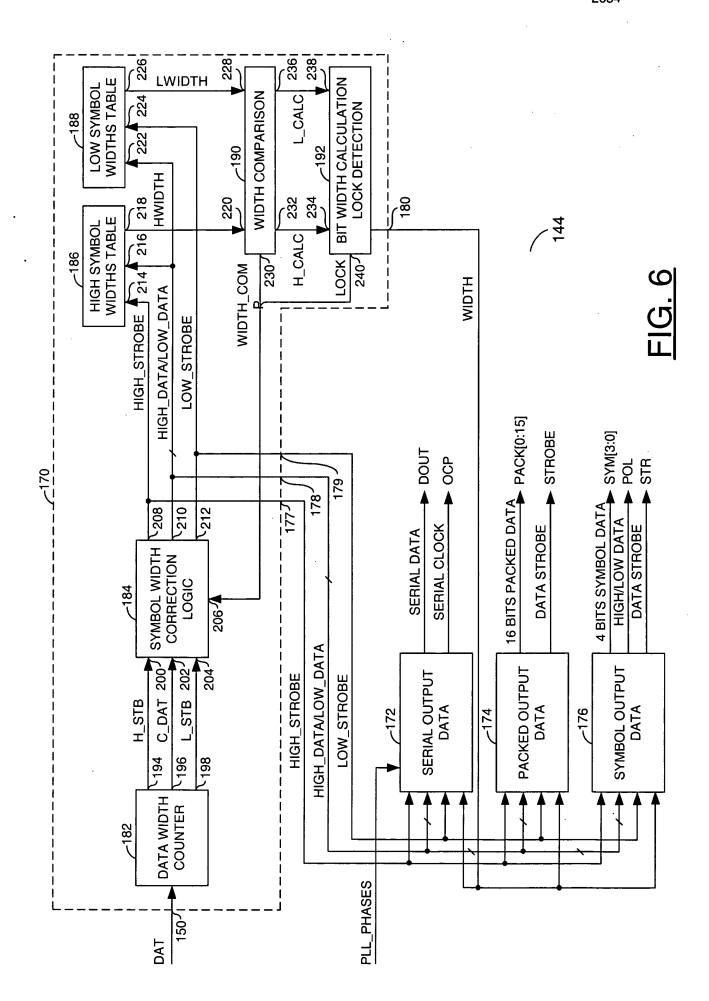
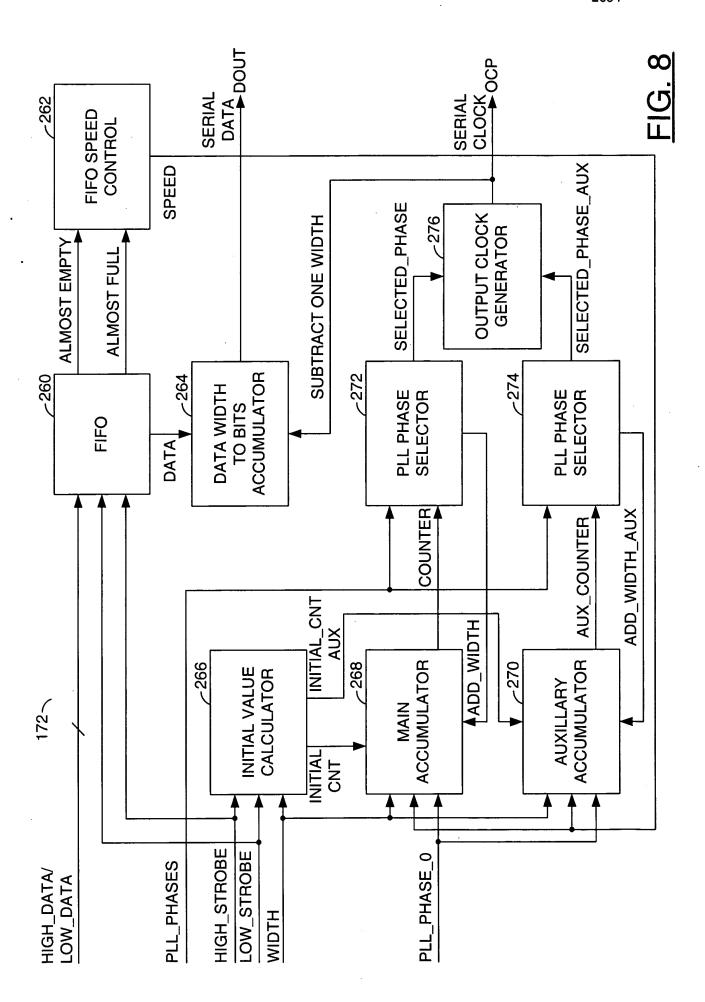
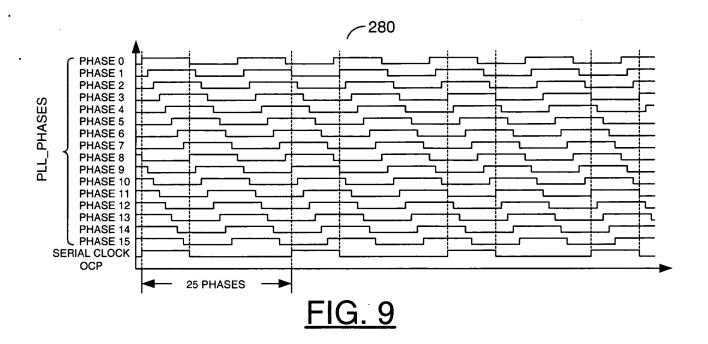


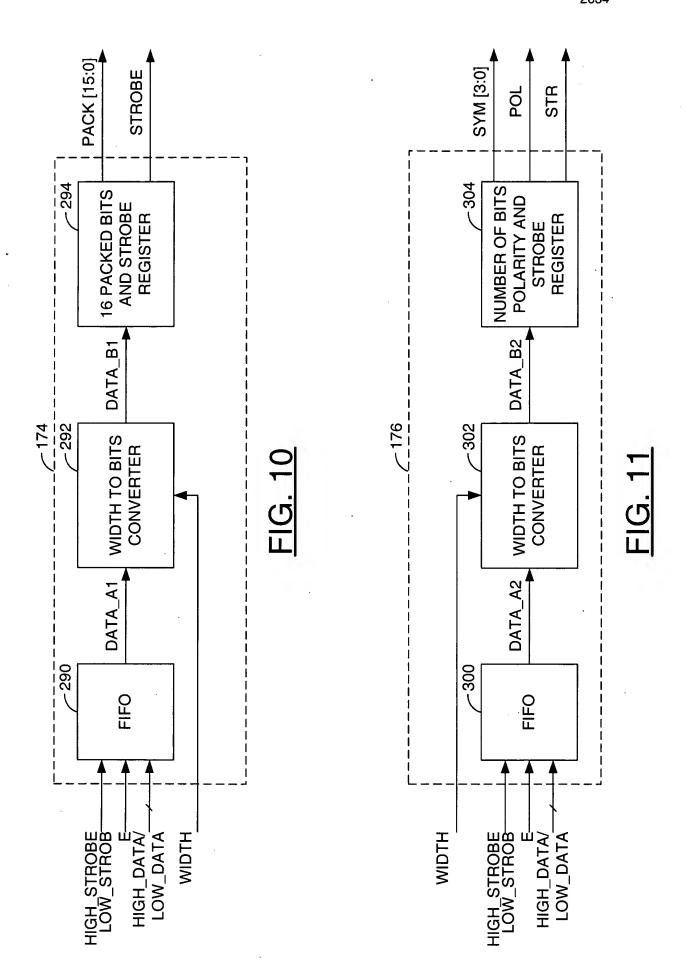
FIG. 5











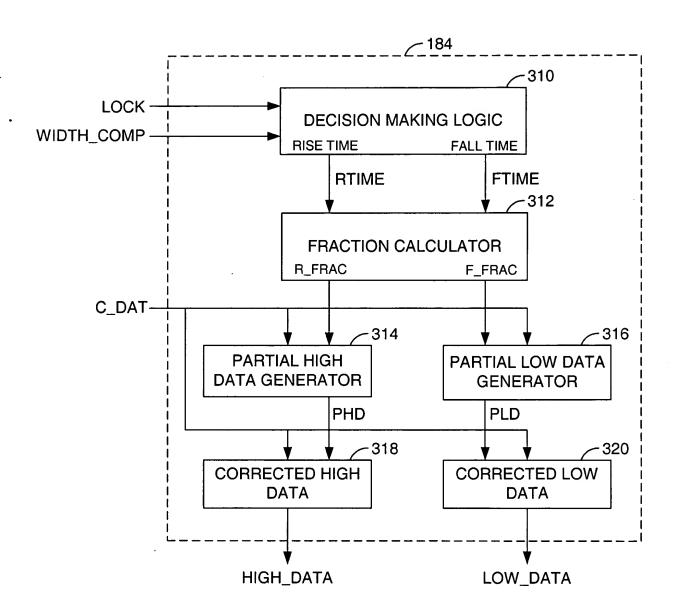


FIG. 12

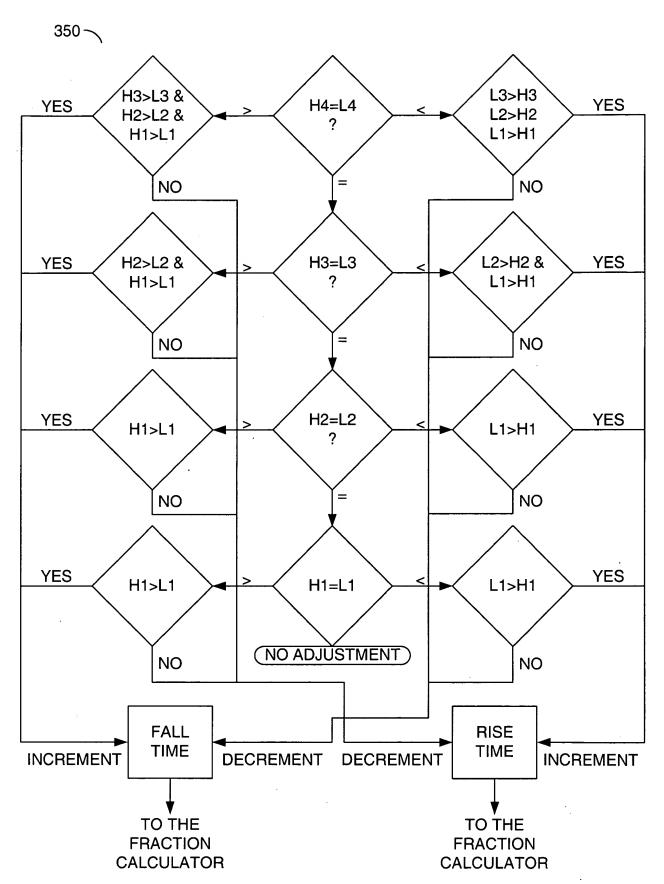


FIG. 13

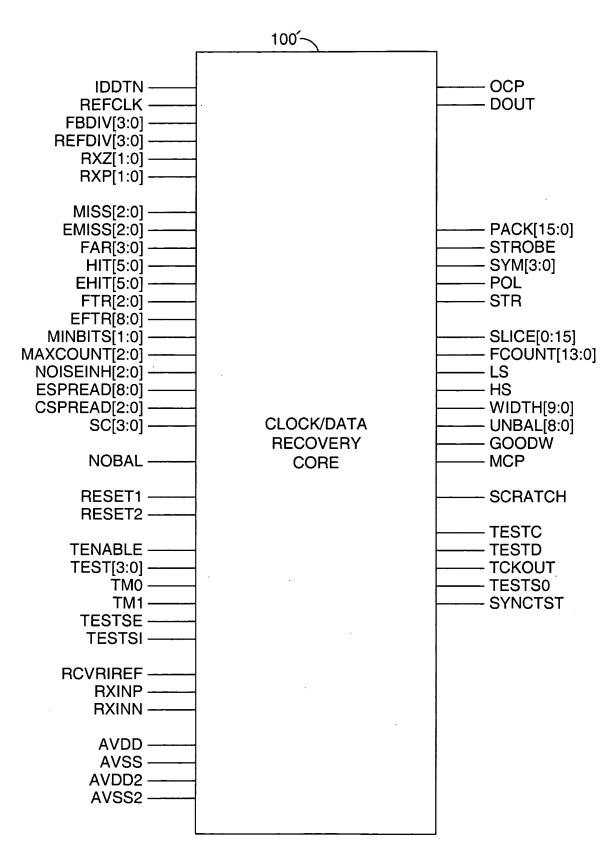


FIG. 14

